

**AMENDMENTS TO THE SPECIFICATION**

Please replace the paragraph beginning at page 1, line 24 with the following edited paragraph:

“According to the invention, in addition to a microprocessor and a communication device, a data-processing system as defined in the opening paragraph-also comprises a hardware circuit allowing inversion or no inversion of the order of bits of a word as a function of the value of the convention signal during transfer of this word between the electronic module and the microprocessor.”

Please replace the paragraph beginning at page 5, line 13 with the following edited paragraph:

“When the data ~~are~~ is transferred from the electronic module [MOD] to the random access memory [RAM], a word of 8 bits is transmitted in series from the electronic module [MOD] to the transfer register device [SIB], one of whose roles is to effect a series-parallel conversion. The hardware circuit [HARD] allows inversion or no inversion of the order of bits of the word at the output of this transfer register device [SIB] as a function of the convention used. The operation of this hardware circuit [HARD] will be described in greater detail with reference to Figs. 4, 5 and 6. The word is sent in parallel to the latched register [LR] whose role is to receive the words from the transfer register device [SIB] before these are used by the random access memory [RAM]. The direct access memory [DMA] extracts the word stored in the locked register [LR] in order to send it to the random access memory [RAM]. When the data ~~are~~ is transferred from the random access memory [RAM] to the electronic module [MOD], the principle is the same as described above. In this case, one of the roles of the transfer register device [SIB] is to realize a parallel-series conversion.”

Please replace the Abstract at page 11  
with the following Abstract:

“A data-processing system comprises a microprocessor, a communication device communicating with an electronic module intended to apply a convention signal to the microprocessor, and a hardware circuit allowing inversion or no inversion of the order of bits of a word as a function of the value of the convention signal during transfer of the word between the electronic module and the microprocessor. Such a system allows a time gain during the operation of converting the bits of a word as a function of the convention used, which is beneficial for the real-time constraint. The system concerned may be used, for example, in a terminal using one or more cards of the SIM type, such as a mobile phone.

Fig. 1”